

Tuesday, 25 February 2014

CLASSES

Class 01:	Introduction in Embedded Linux – a short Crash Course	
09:30-17:00	Introduction to Embedded Linux – a short Crash Course	Robert Berger, Reliable Embedded Systems
Class 02:	Security Fundamentals for Embedded Software	
09:30-17:00	Security Fundamentals for Embedded Software	Dr. David Kalinsky, D. Kalinsky Associates – Technical Training
Class 03:	Workshop on Cryptography and Embedded Security	
09:30-09:45	Welcome	Dr. Thomas Wollinger, ESCRYPT
09:45-10:30	Implementing Long-term Security: Directions and Solutions	Prof. Tim Güneysu, Ruhr-University Bochum
10:30-11:00	Coffee Break	
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-12:45	Implementation Attacks on Wireless Tokens in the Real World	Dr. Timo Kasper, Ruhr-University Bochum
12:45-13:15	Automotive-qualified Hardware Security Modules	Dr. Frederic Stumpf, ESCRYPT
13:15-14:15	Lunch Break	
14:15-14:45	Secure Sensor Communication in Constrained Environments	Dr. Benjamin Glas, Robert Bosch Center of Competence Security
14:45-15:15	Embedded IT Security and Safety in Rail Automation	Matthias Seifert, Siemens Industrial Security
15:15-15:45	Trusted chain with secure boot: from HW, through hypervisor, and up to the virtualised applications	Sergey Tverdyshev, Sysgo
15:45-16:15	Coffee Break	
16:15-16:45	Enhancing security by integrating a Trusted Platform Module (TPM) on a Linux based embedded platform	Dr. Florian Schreiner, Infineon Technologies
16:45-17:15	Securing reconfigurable devices and designs against insiders and other supply chain threats	Tim Morin, Richard Newell, Microsemi
17:15-17:45	Product Security – A Practical Approach	Franz Niedereeder, Fronius

SESSIONS

Session 01:	FPGA & ASIC Design I	
09:30-11:00	FPGA Keynote with Live Demo: The new heart of Embedded Systems – All Programmable SoCs using the example of the Xilinx Zynq device	Stefan Krassin, PLC2
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 01:	FPGA & ASIC Design II	
13:00-13:30	Cortex-M processor based system prototyping on FPGA	Joseph Yiu, ARM
13:30-14:00	In-Circuit FPGA Debug – Challenges and Solutions	James Jeun, Microsemi
14:00-14:30	MIPI Interfaces in Embedded Designs with Low Cost FPGAs	Ted Marena, Lattice Semiconductor
14:30-15:00	Coffee Break	
15:00-15:30	Soft' Analog Solutions for Smart Products	Christian Grumbein, Missing Link Electronics
15:30-16:00	OCLAcc – an Open-source generator for Configurable Logic block based Accelerators	Franz Richter-Gottfried, FAU Erlangen
16:00-16:30	How to evaluate your next IP-Core in the Cloud	Lorenz Kolb, Missing Link Electronics
Session 02:	Software Development in High Level Languages I	
09:30-10:30	Guidelines for Writing Efficient C/C++ Code	Greg Davis, Green Hills Software
10:30-11:00	Modern microcontrollers require modern library solutions for efficient embedded SW development	Georg Huba, Infineon
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 02:	Software Development in High Level Languages II	
13:00-13:30	What you DON'T know can hurt you: What you NEED to know about MISRA C:2012	Mark Pitchford, LDRA
13:30-14:00	Efficient Allocation of Variables to Registers for Architectures with Low Resources	Ciprian Arbore, Freescale
14:00-14:30	Porting C Code to C++ Code	Greg Davis, Green Hills Software
14:30-15:00	Coffee Break	
15:00-16:00	Design Patterns for Embedded Systems in C	Dr. Bruce Douglass, IBM
16:00-16:30	System objects: Design, optimization and C code generation for signal processing in MATLAB	Marco Roggero, The MathWorks
16:30-17:00	Software Optimisation with Intel AVX: Case Study	Liam Walsh, Sergio Gonzalez Monroy, Ircona

Session 03: Software Test & Debug Methods I		
09:30-11:00	System Software Debug on Intel Architecture based Intelligent Systems	Robert Mueller-Albrecht, Paul Farquhar, Intel
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 03: Software Test & Debug Methods II		
13:00-13:30	Connecting without cable to the target – Wireless Debugging as part of an embedded software test concept for corded and cordless electric tools	Erol Simsek, iSYSTEM Dr. Michael Fuchs, Hilti
13:30-14:00	Real-Time Analysis on Processors without Trace Support – efficient Usage of a standard I/O Port for Profiling	Armin Stingl, iSYSTEM
14:00-14:30	Saving money by automated test case-generation from models: How much is it – when will one be happy about having a break even?	Thomas Franke, AFRA
14:30-15:00	Coffee Break	
15:00-16:00	Tips and Tricks for Debugging	Greg Davis, Green Hills Software
16:00-16:30	Model Based Test Case Generation for Automated Software Test in Safety Critical Applications	Ingo Nickles, Vector Cast
Session 04: Tutorial: FPGAs for Software Engineers		
09:30-11:00	De-Mystifying FPGAs for Software Engineers	Glenn Steiner, Xilinx
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 05: Designing for Ultra-Low Energy		
13:00-13:30	Ultra-Low Energy Considerations when developing an MCU Application	Johannes Zipperer, Texas Instruments
13:30-14:00	Make Battery Backup Operation a Strength for More Secure, Reliable Products	Jacob Borgeson, Smart Modular Technologies
14:00-14:30	Power Management in Embedded Systems	Colin Walls, Mentor Graphics
14:30-15:00	Coffee Break	
15:00-15:30	Considerations for Ultra Low Power Real Time Operating Systems	Michael Brunotte, Günther Höne, Texas Instruments
15:30-16:00	Wireless Sensor Nodes using Ultra-Low-Power FRAM Microcontrollers	Volker Rzehak, Texas Instruments
16:00-16:30	Power consumption software estimation	Razvan Ionescu, Freescale Semiconductor
Session 06: Wireless Technologies I		
09:30-10:15	All Wireless – Integrating wireless charging, antenna coupling and near field communication for smart phones in cars	Prof. Dr. Peter Fromm, University of Applied Sciences Darmstadt
10:15-11:00	Advances in wireless communication	Prof. Dr. Axel Sikora, Offenburg University of Applied Sciences
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 06: Wireless Technologies II		
13:00-13:30	Using Bluetooth Low Energy as a Wireless Connectivity Technology in Embedded Applications	Prof. Dr. Gerald Kupris, Deggendorf Institute of Technology (DIT)
13:30-14:00	Bluetooth Low Energy and Development Platforms from Apple, Google, Microsoft and RIM	Rudi Latuske, ARS Software
14:00-14:30	Comparing the energy requirements of current Bluetooth Smart solutions	Prof. Dr. Marcel Meli, Jachen Bernegger, ZHAW INES
14:30-15:00	Coffee Break	
15:00-15:30	Implementation issues of BSI-enabled wireless metering	Fesseha Tsegaye Mamo, Offenburg University of Applied Sciences
15:30-16:00	Development of a Flexible Application Layer for Wireless M-Bus, OMS and DSMR Protocols	Naksit Anantalapochai, Offenburg University of Applied Sciences
16:00-16:30	Explore Hardware and Software Strategies to Design Smart Energy Profile (SEP) 2.0 Compliant Devices	Andrew Caples, Mentor Graphics
Session 07: Tutorial: Cortex-M		
09:30-11:00	Create a microcontroller application using Cortex-M processors and CMSIS components	Matthias Hertel, ARM Germany
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software
12:00-13:00	Lunch Break	
Session 08: Software Quality		
13:00-13:30	Which two will your team pick: High quality software, on time delivery or within budget?	Mark Richardson, LDRA
13:30-14:00	Efficiently Proving the Absence of Stack Overflows in Safety-Critical Embedded Software	Dr. Daniel Kästner, AbsInt
14:00-14:30	An advanced modular and portable Test Automation Framework for practical use	Kristian Trenkel, iSyst Intelligente Systeme
14:30-15:00	Coffee Break	
15:00-15:30	Not Tracing Requirements? Maybe You're Building in Defects	Mark Pitchford, LDRA
15:30-16:00	Beyond MC/DC Coverage Testing	Dr. Stefan Häußler, BTC Embedded Systems
16:00-16:30	NASA JPL leverages Coverity for Compliance with their Institutional Coding Standard	Stefan Asbock, Coverity
Session 09: Panel Discussion: Multicore processors for embedded systems: Are we ready?		
09:30-11:00	Multicore processors for embedded systems: Are we ready?	Prof. Dr. Jürgen Teich, FAU Erlangen-Nürnberg / ESI Heinz Wrobel, Freescale Semiconductor Glenn Farrall, Infineon Technologies
11:00-12:00	Conference Keynote: Securing the Internet of Things	David Kleidermacher, Green Hills Software

Wednesday, February 26, 2014

CLASSES

Class 04:	Hands-on Introduction to Linux RTOS	
09:30-17:00	Hands-on Introduction to Linux RTOS	Prof. Nicholas Mc Guire, Andreas Platschek, OSADL Safety Critical Linux Working Group
Class 05:	Safety-critical Systems Design	
09:30-17:00	Safety-critical Systems Design	Dr. David Kalinsky, D. Kalinsky Associates – Technical Training
Class 06:	Industrial Security Workshop	
09:30-12:30	Industrial Security Workshop	Prof. Dr. Peter Fröhlich, Karl Leidl, Deggendorf Institute of Technology (DIT)
Class 07:	Hands-On Workshop: Applying Optimization Techniques for Ultra-Low Power Microcontrollers	
09:00-09:15	The Complexities and Challenges of Ultra-Low Power Measurement and Development	Markus Levy, EEMBC
09:15-09:40	Differentiating and Optimizing for Static and Active Microcontroller Modes	Blaise Lengrand, Atmel
09:40-10:05	Energy Measurement Techniques for Ultra-Low Power Design	Eduardo Montañez, Freescale Semiconductor
10:05-10:30	Diagnosing and Modifying Energy-Consuming Code	Brant Ivey, Microchip
10:30-11:00	Sponsored Morning Break – sponsored by Rutronik	
11:00-11:25	Using Peripherals to Reduce System Energy Consumption	Mark Cullum, Renesas Electronics
11:25-11:50	Why Microcontroller Operating Frequency Matters for ULP	Mark Wallis, STMicroelectronics
11:50-12:15	Fine-Tuning an MCU-based Application for Power Efficiency	Priya Thanigai, Texas Instruments
12:15-13:15	Lunch break – sponsored by Renesas	
13:15-13:30	The Roadmap for Ultra-Low Power Applications	
13:30-14:30	Hands-on lab session 1	
14:30-15:30	Hands-on lab session 2	
15:30-16:00	Sponsored Afternoon Break – sponsored by Texas Instruments	
16:00-17:00	Hands-on lab session 3	
Class 08:	Embedded Android Workshop	
09:30-17:30	Embedded Android Workshop	Karim Yaghmour, Opersys
Class 09:	Protect an Embedded System against Tampering and Counterfeiting	
13:30-17:00	Protect an Embedded System against Tampering and Counterfeiting	Oliver Winzenried, Wibu-Systems

SESSIONS

Session 10:	Security & Cryptography I	
13:30-14:00	Data Protection and Data Security: a lawyer's view on recent trends after Snowden	Prof. Dr. Nikolaus Forgó, Institute for Legal Informatics Leibniz Universität Hannover
14:00-14:30	Joint Safety and Security in Embedded Systems	John Favaro, INTECS
14:30-15:00	Safety and Security in the Multi-Core Age	Matthias Pruksch, sepp.med
15:00-15:30	Coffee Break	
15:30-16:00	Security in Cyber Physical Systems CPS	Michael Wagner, Fraunhofer IIS
16:00-16:30	Risk analysis for Industrial IT-Security	Dr. Thomas Störckuhl, TÜV SÜD
16:30-17:00	Security Considerations for Internet of Things	Michael Cioffi, McAfee
Session 11:	Yocto	
09:30-10:00	Next Generation Embedded Systems: Building an Embedded Platform with Yocto	Ken Sharp, National Instruments
10:00-10:30	Making a Splash: Digital Signage Powered by MinnowBoard and the Yocto Project	Scott Garman, Intel
10:30-11:00	Using the Yocto Project to Conquer the IoT Security Nightmare	David Stewart, Intel
11:00-11:30	Coffee Break	
Session 12:	Embedded Linux I	
11:30-12:00	Linux Fast Boot: Techniques for Aggressive Boot Time Reduction	Andrew Patterson, Mentor Graphics Embedded SW Division
12:00-12:30	Striking the Right Balance: Combining Proprietary and Open Source Software in Your Embedded Project	Chris Ault, QNX Software Systems
12:30-13:30	Lunch Break	

Session 12:		Embedded Linux II
13:30-14:00	Linux Mainlining: benefits and process?	Antoine Tenart, Adeneo Embedded
14:00-14:30	Flash Friendly File Systems	Thom Denholm, Datalight
14:30-15:00	Jailhouse – A Linux-based Partitioning Hypervisor	Jan Kiszka, Siemens
15:00-15:30	Coffee Break	
15:30-16:00	Linux-based Automotive Software: Some Unexpected Challenges, Some Interesting Solutions	Andrew Patterson, Mentor Graphics Embedded SW Division
16:00-16:30	Porting openSUSE to 64-bit ARMv8	Andreas Färber, SUSE LINUX
16:30-17:00	Linux debugging tools	Antoine Tenart, Adeneo Embedded

Session 13:		Embedded System Design Automation I
09:30-10:00	Hardware/Software Co-Development – Challenges and Opportunities for EDA	Frank Schirrmeister, Cadence Design Systems
10:00-11:00	De-Mystifying HW & SW Design Partitioning with All Programmable SoCs	Dan Isaacs, Xilinx
11:00-11:30	Coffee Break	
11:30-12:30	A practical introduction to 'hybrid prototyping'	Juergen Jaeger, Cadence Design Systems
12:30-13:30	Lunch Break	

Session 13:		Embedded System Design Automation II
13:30-14:00	A Closer Look at AUTOSAR Design Automation	Armin Lichtblau, Mentor Graphics
14:00-15:00	Case Study: Driving the Adoption of Test Automation in Development	Dr. Andreas Kuehlmann, Coverity
15:00-15:30	Coffee Break	
15:30-16:00	Virtual Platform/Emulation Hybrid: The best of both worlds; high performance software execution and RTL accuracy for the design increases performance for system validation	Frank Schirrmeister, Cadence Design Systems
16:00-16:30	Pre-Silicon Software Development	Russell Klein, Mentor Graphics
16:30-17:00	How an integrated ESL Design Flow can cut-down efforts for Algorithm Exploration, HDL Co-Simulation and FPGA implementation	Ingo Nিকেleit, Agilent Technologies

Session 14:		Model based Design I
09:30-11:00	Bruce's Top Ten Modeling Hints: The Secrets to Effective Modeling	Dr. Bruce Douglass, IBM
11:00-11:30	Coffee Break	
11:30-12:00	Streamlined Model-Driven Performance Engineering for Embedded Software and Systems	Dr. Connie Smith, L&S Computer Technology
12:00-12:30	Automatic Evaluation of Model Quality with INProVE	Dr. Thomas Kuhn, Fraunhofer IESE
12:30-13:00	Combining the power of DAVE and Simulink – from a High Level Model to Embedded Implementation	Pedro Costa, Infineon
13:00-13:30	Lunch Break	
14:00-14:30	Configurable hardware for electronic control units: A challenge and a chance for model based software development	Sebastian Grobosch, VEMAC
14:30-16:30	Agile Model-Driven Development for Embedded Systems	Dr. Bruce Douglass, IBM
16:30-17:00	High Level Methodologies in Embedded System Design	Etienne Brosse, Softeam R&D

Session 15:		Embedded GUI Development I
09:30-10:00	Mobile & pure web: a revolution in HMI design?	Andreas Beu, Smart HMI
10:00-10:30	Graphics GUI in low cost Embedded Systems	Ivo Cisar, STMicroelectronics
10:30-11:00	Coffee Break	
11:00-11:45	Building a Smartphone-Class User Experience into Your Embedded Device	Chris Ault, QNX Software Systems
11:45-12:30	Effective internationalization for Embedded Systems	Tobias Kniep, Zühlke
12:00-13:30	Lunch Break	

Session 15:		Embedded GUI Development II
13:30-14:00	Developing the Next Generation Embedded HMIs	Andrew Patterson, Mentor Graphics Embedded SW Division
14:00-14:30	Using the Android Platform for Embedded User Interface Development	Jacob Maxa, University of Rostock
14:30-15:00	Automated UI Testing on Embedded Systems	Reginald Stadlbauer, froglogic
15:00-15:30	Coffee Break	
15:30-16:00	Develop accelerated Open GL QML components	Adrien Leravat, Adeneo Embedded
16:00-16:30	Using modern web technologies to accelerate embedded system UI development	Olaf Christ, mycable
16:30-17:00	Analyzing and Solving UI Performance Problems on Embedded Hardware	Phil Brumby, Mentor Graphics Embedded SW Division

Session 16:	Wireless Technologies III	
09:30-10:00	Evaluation of Car-to-x platforms for integration into future serial production vehicles	Christian Payerl, Magna Steyr Engineering
10:00-10:30	Benefits and limitations by replacing high-speed data links with a modern optical wireless solution	Michael Faulwaßer, Fraunhofer IPMS
10:30-11:00	Coffee Break	
11:00-11:30	Low Power? No Power! Energy-harvesting wireless sensor nodes enabling an Internet of Things	Matthias Kassner, EnOcean
11:30-12:00	Real Time Analysis of Radio Communication in Fading Environments	Arne Neiser, Rostock University
12:00-12:30	Transceiver for Wireless Sensor Networks (WSN) using a SDR Ultra-Integrated Platform with FPGA-Processing based on Direct-Conversion-Technique	Gonzalo Asencio, AED Engineering
12:30-13:30	Lunch Break	
Session 16:	Wireless Technologies IV	
13:30-14:00	Short-range Low Power Wireless Devices and Internet of Things	Martin Engdahl, connectBlue
14:00-14:30	Sonic propagation time measurement for tomography with wireless connected ARM Cortex-M3 micro-controllers	Matthias Terber, University of Applied Sciences Giessen
14:30-15:00	The Internet of Things for the connected home	Colin Faulkner, NXP Semiconductors
15:00-15:30	Coffee Break	
15:30-16:00	Low Power and RF technologies paving the way for the growth of internet of things applications	Matt Saunders, Silicon Labs
16:00-16:30	Development of an energy autonomous water meter supporting Wireless M-Bus Mode Q and P	Prof. Dr. Axel Sikora, Offenburg University of Applied Sciences
16:30-17:00	Simultaneously Connecting Devices through Bluetooth Smart	Andreas Rüst, ZHAW InES

Thursday, February 27, 2014

CLASSES

Class 10:	GNU/LINUX for safety critical systems	
09:30-17:00	GNU/LINUX for safety critical systems	Prof. Nicholas Mc Guire, Andreas Platschek, OSADL Safety Critical Linux Working Group
Class 11:	Software Design for Multicore Systems – 2014 Edition	
09:30-17:00	Software Design for Multicore Systems – 2014 Edition	Dr. David Kalinsky, D. Kalinsky Associates – Technical Training
Class 12:	JAVA	
09:30-12:30	Desktop to Internet of Things in 12 Seconds with Java ME Embedded	Terrence Barr, Oracle
Class 13:	Workshop Signal Integrity	
13:30-14:00	Special construction principles for reliable Highspeed-PCBs	Arnold Wiemers, Leiterplatten Akademie
14:00-14:30	Jitter in PCIe application on embedded boards with PLL Zero delay Clock buffer	Hermann Ruckerbauer, Eye KnowHow
14:30-15:00	Coffee Break	
15:00-15:30	How to anticipate Signal Integrity Issues: Improve my Channel Simulation by using Electromagnetic based model	Ludwig Eichinger, Agilent Technologies
15:30-16:00	Electrical Modeling and Optimization of Discontinuities along Signal Paths in Electronic Systems using the M3-Approach	Dr. Brian Curran, Fraunhofer IZM

SESSIONS

Session 17:	Security & Cryptography II	
09:30-10:00	Next Generation Security Technologies for Embedded Systems	Michael Cioffi, McAfee
10:00-10:30	IT Security for embedded system – functional safety is not enough	Markus Bartsch, TUViT
10:30-11:00	Coffee Break	
11:00-11:30	How to Bridge the Gap in Secure Software Development	Hartmut Goebel, (ISC) ²
11:30-12:00	Technologies and Techniques for Securing Connected Devices	Chris Conlon, wolfSSL
12:00-12:30	Making Architectural Decisions about Smart System Security: How Risky is Risk?	Dr. David Jackson, Altran
12:30-13:30	Lunch Break	
Session 17:	Security & Cryptography III	
13:30-14:00	Security Framework for IP based wireless networks	Mike Ludwig, dresden elektronik ingenieurtechnik
14:00-14:30	Protecting Your Embedded System Boot Process from Network Threats	Tim Morin, Microsemi
14:30-15:00	Coffee Break	
15:00-15:30	Cyber security, secure boot and firmware update, licensing and know how protection	Oliver Winzenried, Wibu-Systems
15:30-16:00	Trust Me, I'm an M2M Device	Terrence Barr, Oracle
16:00-16:30	Security for Cloud Computing: Understanding Security Challenges	Todd Moore, IBM
Session 18:	Functional Safety Systems I	
09:30-10:30	Safety Critical Systems Design with UML	Martin Stockl, IBM
10:30-11:00	Coffee Break	
11:00-11:30	Survive ISO26262 with model-based development!	Dr. Oliver Alt, LieberLieber Software
11:30-12:00	ISO 26262 – Safety Element out of Context (SEooC) in Practise	Gudrun Neumann, SGS-TÜV Saar
12:00-12:30	Safety Impact on Switching Costs	Randall Restle, Digi-Key
12:30-13:30	Lunch Break	
Session 18:	Functional Safety Systems II	
13:30-14:00	Functional Safety and Industry 4.0	Dirk Bilgram, HIMA Paul Hildebrandt
14:00-14:30	Aspects of Safety-Critical Distributed Embedded-Systems	Olaf Winne, Lamtec Leipzig
14:30-15:00	Coffee Break	
15:00-15:30	Make a Cortex M3 project to a safety project with CortexR4	Dr. Kurt Böhringer, Hitex Development Tools
15:30-16:00	Looking under the hood of a pre-certified real-time operating system	Dr. Björn Sander, Hitex Development Tools
16:00-16:30	Software Architectures for Safety Projects using Multicore Processors in an AUTOSAR environment	Alexander Much, Elektrotbit Automotive
Session 19:	Internet & Communication I	
09:30-10:00	Challenges in Automotive Connectivity: Devices, Technologies and the Connected Car	Andrew Patterson, Mentor Graphics Embedded SW Division
10:00-10:30	Ethernet Performance Metrics for In-Vehicle Applications	Dr. Simon Schliecker, Syntavision
10:30-11:00	Coffee Break	
11:00-11:30	Ethernet over noisy channels	Christian Liss, InnoRoute
11:30-12:00	Converged Ethernet – Improvements to Standard Ethernet will Enable High Performance Control on COTS Ethernet	Todd Walter, National Instruments
12:00-12:30	Low-Latency Networking for Systems-of-Systems	Dr. Endric Schubert, Missing Link Electronics
12:30-13:30	Lunch Break	

Session 19:	Internet & Communication II	
13:30-14:00	Distributed, Networked Embedded Control Approaches for Efficient, Reliable Mobile Systems	Prof. Dr. Sergio Montenegro, University of Würzburg
14:00-14:30	Taking Mobile Devices into Industrial Embedded Systems	Andy Walter, macio
14:30-15:00	Coffee Break	
15:00-15:30	Building Web Based Applications for Embedded Systems	Brian Kindinger, National Instruments
15:30-16:00	Applying Standards Based IMS Communication for Voice and Video in Embedded Applications	Joakim Hedenstedt, Movial
Session 20:	Multicore Processors I	
09:30-10:00	Multicore Processors: Challenges, Opportunities, Emerging Trends	Prof. Dr. Christian Märtin, University of Applied Sciences Augsburg
10:00-10:30	Migration to multicore processing, trends, techniques and industry case studies	Heinz Wrobel, Freescale Semiconductor
10:30-11:00	Coffee Break	
11:00-11:30	Establishing a portable interface between hardware and software in multicore systems	Markus Levy, EEMBC Masaki Gondo, eSOL
11:30-12:00	Interprocessor Communications and MCAP1	Colin Walls, Mentor Graphics
12:00-12:30	Operating System Concepts for Embedded Multicores	Oliver Horst, Fraunhofer ESK
12:30-13:30	Lunch Break	
Session 20:	Multicore Processors II	
13:30-14:00	Bootling and running multiple operating systems (OS) simultaneously on Embedded Multicore SoCs	Nils Bürkner, mycable
14:00-14:30	Supercharging Embedded Systems with Parallel Computing	Andreas Olofsson, Adapteva
14:30-15:00	Coffee Break	
15:00-15:30	Multicore Development Paradigms	Greg Davis, Green Hills Software
15:30-16:00	Interference in time, analysis and optimization options on a multi-core microcontroller	Claus Stellweg, Elektrobit Automotive Jens Harnisch, Infineon Technologies
16:00-16:30	Avoiding Risks in First-Generation Multi-Core Designs through Timing-Aware Software Development	Dr. Simon Schliecker, Syntavision
Session 21:	Android I	
09:30-10:30	Is Android the new king of embedded OSes?	Karim Yaghmour, Opersys
10:30-11:00	Coffee Break	
11:00-11:30	Including Android into Your Infotainment System	Andrew Patterson, Mentor Graphics Embedded SW Division
11:30-12:00	Qt on Android platforms	Adrien Leravat, Adeneo Embedded
12:00-12:30	Optimizing the cocos2d-x Android library: a DS-5 Streamline case study	Lukas Snetler, ARM
12:30-13:30	Lunch Break	
Session 21:	Android II	
13:30-14:30	Android under Cover – In other words: Do you want everybody to mess with your code?	Michael Zunke, Safenet
14:30-15:00	Coffee Break	
15:00-15:30	How to Make Android-based Devices Secure	Andre Schmitz, Green Hills Software
15:30-16:00	Developing Secure Embedded Applications in Embedded Android	Mike Rohrmoser, Digi International
16:00-16:30	Hypervisor-based Consolidation for Automated Teller Machines	Stefan Groesbrink, University of Paderborn Andre Schmitz, Green Hills Software
Session 22:	Development Tools	
09:30-10:30	Advanced Compiler Optimizations for the Smallest, Fastest Code	Greg Davis, Green Hills Software
10:30-11:00	Coffee Break	
11:00-11:30	Toolchain Architectures for Embedded Systems	Thomas Schuetz, Protos Software
11:30-12:00	AUTOSAR from a Developer's Point of View	Martin Thiede, Martin Mössmer E.S.R. Labs
12:00-12:30	Optimizing Automotive Software Stacks for Performance and Reliability	Naveen Gv, Yang Wang, Intel
Session 23:	Verification & Simulation	
13:30-14:00	Verification Techniques for Better Code and Higher Productivity	Mark Richardson, LDRA
14:00-14:30	Customized, Intelligent Memory Access Monitoring for Reliable Asymmetric Multi-Processor System Development	Larry Lapides, Imperas
14:30-15:00	Coffee Break	
15:00-15:30	Semi-formal Modeling of Simulation-based V&V Methods to Enhance Safety	Martin Krammer, Virtual Vehicle Research Center
15:30-16:00	Taint Analysis for finding Programming Defects	Dr. Paul Anderson, GammaTech
16:00-16:30	From the Model to the Target to Certification	Mark Richardson, LDRA

embedded world Conference 2014

25.-27.2.2014, Nuremberg, Germany



Session 24:	NFC/RFID	
09:30-10:00	Mobile Innovations with NFC	Prof. Dr. Ralf S. Mayer, University of Applied Sciences Darmstadt
10:00-10:30	NFC Hardware and Middleware Integration	Jerome Pele, NXP
10:30-11:00	Coffee Break	
11:00-11:30	NFC Integration in Embedded Systems	Claudius Kehrhahn, Stollmann E+V
11:30-12:30	EMC related design of NFC devices	Renke Bienert, NXP Semiconductors
Session 25:	M2M I	
09:30-10:00	Security – the soft underbelly of the M2M revolution	John Blevins, LynuxWorks
10:00-10:30	Secured wireless M2M communication for medical environments	Dr. Thomas Brumm, b1 Engineering Solutions
10:30-11:00	Coffee Break	
11:00-11:30	Applying connectivity, manageability and security to critical infrastructure	Michael Gaudlitz, Wind River
11:30-12:00	M2M and Embedded Processing: Advantages of Multi-core & Asymmetric Processing	Dr. Stephane Gervais-Ducouret, Freescale Semiconductor
12:00-12:30	M2M – how to overcome integration barrier	Thomas Randt, Telit Wireless Solutions
Session 25:	M2M II	
13:30-14:00	Architecture of the building blocks of the Internet of Things (IoT), from the edge sensing nodes to the cloud computing	Iain Davidson, Freescale Semiconductor
14:00-14:30	Building sensor networks using Node.js and cloud technologies	Cevahir Turgut, Aselsan
14:30-15:00	Coffee Break	
15:00-15:30	Building a first Domotic SensorCloud Node	Prof. Dr. Georg Hartung, Cologne University of Applied Sciences
15:30-16:00	Cloud-enable your embedded device (e.g. Raspberry Pi) using REST APIs	Stefan Vaillant, Cumulocity

